

EXAMINER'S AMENDMENT

This application is in condition for allowance except for the presence of claims 4, 7-11 and 18, which read on a non-elected species. An election has been made without traverse by Applicant on July 30, 2007. Accordingly, these claims have been canceled below by authority of the Examiner.

Also claim 19 appears to lack antecedence for "the electrode pad unit", thus it is fixed by this examiner amendment as follow:

The application has been amended as follows:

Claims:

Claims 4, 7-11 and 18 are canceled.

In line 4 of claim 19, "***an electrode unit***" has been changed to __ ***an electrode pad unit***__

An examiner's amendment to the record appears above. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

DETAILED ACTION

Allowable Subject Matter

Claims 1-3, 5, 6, 12-17, 19 and 20 are allowed.

Reasons for Allowance

Claims 1-3, 5, 6, 12-17, 19 and 20 are allowable because there is no prior art of record that either teaches or suggests a liquid crystal display device comprising the following specific limitations recited in:

Claims 1 and 19:

an electrode pad unit is directly connected to the first switching circuit and the second switching circuit

Conclusion

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Maeda et al (US 20030030615 A1) disclose a matrix image display device of the present embodiment includes a pixel array 1 as a display section, a scanning signal line driving circuit 2 and a data signal line driving circuit 3, which are monolithically formed on a single insulating substrate 11. Thus monolithically forming the scanning signal line driving circuit 2 and the data signal line driving circuit 3 on the same

substrate provides an effect of lower cost compared to the case where those driving circuits are respectively formed and mounted, and also results in an effect of high-reliability.

Morita et al (US 20020190944 A1) disclose a high image quality and a low power consumption compatible to provide a scan-driving circuit suitable for the active matrix type liquid crystal panel, and a display device, an electro-optical device and a scan-driving method using the signal drive circuit.

Yamazaki et al (US 20020079484 A1) disclose a semiconductor display device correcting system and correcting method of semiconductor display device. A semiconductor display device correcting system includes a control circuit for carrying out gamma correction of a picture signal supplied from the outside and a nonvolatile memory for storing data for gamma correction. The data for gamma correction is prepared for each semiconductor display device, so that excellent gradation display can be made.

Contact Information

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Julie-Huyen L. Ngo whose telephone number is (571) 272-2295. The examiner can normally be reached on M-Thursday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Nelms can be reached on (571) 272-1787. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Art Unit: 2871

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Julie-Huyen L. Ngo/
Primary Examiner
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